**E.G.S PILLAY ENGINEERING COLLEGE, NAGAPATTINAM**

**DEPARTMENT OF MCA**

**SAT II**

Sub.Code & Name: MC9211 COMPUTR ORGANIZATION Date :14.11.2013

Year & Sem : I/I Time : 1hr(9.00am-9.50am)

Staff Name : Mr. S .Selvaganapathy Max. Marks : 30

**Answer all the Questions (15 x 2 =30)**

1. Draw a half adder circuit using logic gates
2. What is mean by half adder and full adder circuit?
3. What is mean by half subtractor and full subtractor circuit?
4. What is mean by Encoder?
5. What is mean by Decoder?
6. Define MUX and DEMUX.
7. Define Register and shift Register?
8. Implement the following Boolean function using 8:1 MUX

F(A,B,C,D) = ∑M (0,1,3,4,8,9,15)

1. Define Flip Flop
2. Mention the basic types of Flip - Flop?
3. Mention the classification of counters?
4. Differentiate between DRAM & SRAM
5. What is pipelining?
6. Mention the types of Addressing modes?
7. Define immediate addressing mode?